

Technical Specification

Fieldbus communication protocol definition for LED-Controller CTR-52

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Version	Author	Date	Changes	Status
1.01	B. Kuhrmann	May 11 th 2022	Initial Release	Valid

Hardware

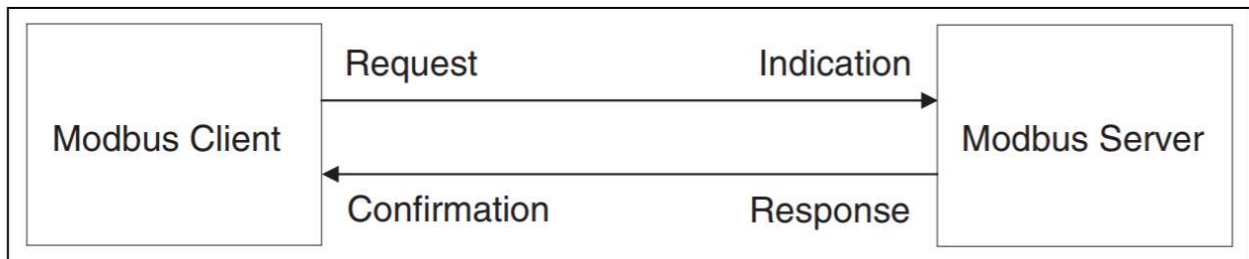
The standard fieldbus provided with the LED controller CTR-52 product line, is based on Modbus TCP. Modbus is a data communications protocol for use with its programmable logic controllers (PLCs).

Modbus Setup Information

The LED controller CTR-52 is a Modbus device that allows you to access the light settings via Ethernet and communicates using a master-slave technique in which only one device (the master) can initiate transactions (called queries). The other devices (slaves) respond by supplying the requested data to the master, or by taking the action requested in the query.

The CTR-52 controller is implemented as a Modbus slave (server). The server receives messages from the master (client), processes them and responds to them. The product does not send messages by itself.

Modbus Type: Slave (Server)
 Modbus Format: Modbus TCP
 Output Data Mode: Auto



Field name	Description
Register	The Modbus register address
Point	The number of the I/O point
Description	The function description for the Modbus register
Data Format	Indicates the type of data in the Modbus register
Attribute	Indicates the attribute of Modbus register R: Read W: Write E: Save to the EEPROM

Modus Register Table

Coils: Function codes 01 (read) 05/15 (write)

Address (decimal)	Points	Description	Data format	R/W/E	Factory Default
00	1	Restore all settings to default (does not modify EEPROM). Is set back to 0 by the controller	1: reset	W	-
1	1	Switching on and off the voltage control for STEADY mode	0: regulation off 1: regulation on	R/W/E	1
2	1	Switching on and off the voltage control for FLASH mode	0: regulation off 1: regulation on	R/W/E	0
3	1	Switching on and off the voltage control for AUTO mode	0: regulation off 1: regulation on	R/W/E	1
04 - 19	19	Reserved	-	-	-
Channel specific					
20	1	Channel 0: Software trigger, overrules hardware input only if hardware signal is low	0: low 1: high	W	-
21	1	Channel 0: Tune voltage to target current. Is set back to 0 by the controller.	1: start tuning	W	-
22	1	Channel 0: Set trigger line to inverted	0: not inverted 1: inverted	R/W/E	0
23	1	Channel 0: Enable voltage regulation The regulation is only switched on if the corresponding mode-specific regulation (coil 1, 2 or 3) is also switched on.	0: regulation off 1: regulation on	R/W/E	1
24 - 29	7	Reserved	-	-	-
30 – 39	10	Channel 1 specific			
40 - 49	10	Channel 2 specific			
50 - 59	10	Channel 3 specific			

Discrete inputs: Function code 02 (read single bits)

Address (decimal)	Points	Description	Data format	R/W/E	Factory Default
00	1	Alive bit. Toggles every 500ms	-	R	-
01 - 19	19	Reserved	-	-	-
Channel specific					
20	1	Light source detected	0: no light source attached 1: light source attached	R	-
21 – 29	9	Channel 0 specific (Reserved)	-	-	-
30 – 39	10	Channel 1 specific			
40 – 49	10	Channel 2 specific			
50 - 59	10	Channel 3 specific			

Holding Register: Function codes 03 (read) 06/16 (write)

Address (decimal)	Points	Description	Data format	R/W/E	Factory Default
00 - 01	2	IP Address 192.168.0.99 Valid after reboot	00: high byte: 99 00: low byte: 0 01: high byte: 168 01: low byte: 192	R/W/E	See left
02 - 03	2	Subnet Mask 255.255.255.0 Valid after reboot	02: high byte: 0 02: low byte: 255 03: high byte: 255 03: low byte: 255	R/W/E	See left
04	1	Store current settings by setting the magic number: 12345 (0x3039)	0: do nothing 12345: store all settings to EEPROM		
05	1	Minimal time gap to next trigger after flash is off [μ s]	μ s	R/W/E	
06 – 09	4	Reserved	-	-	
10	1	Internal command: set production week. Can be set after setting access key in holding register 11	High byte: year Low byte: week	R/W/E	-
11	1	Access key (0x6E09) to set private data (holding registers 10 and 12)	28169: allow setting else: values cannot be changed	W	0

12 – 14	3	Internal command: MAC Address e.g. 70-B3-D5-E0-30-12 Can be set after setting access key in holding register 11 Valid after reboot	12: low byte: 70 12: high byte: B3 13: low byte: D5 13: high byte: E0 14: low byte: 30 14: high byte: 12	R/W/E	-
15 - 19	5	Reserved	-	-	-
Channel specific					
20	1	Channel 0: Operation Mode	0: OFF 1: STEADY ON 2: FLASH MODE (on rising edge) 3: AUTO (Light on while trigger line set)	R/W/E	2
21	1	Channel 0: Trigger input lines in AUTO and FLASH mode: Start the flash, when either of the input lines is set high. E.g.: respond to Trigger line 0 or 3 are set: 1+8 = 9 If set, Address 22 is set to 0	1: Trigger line 0 2: Trigger line 1 4: Trigger line 2 8: Trigger line 3	R/W/E	same as channel
22	1	Channel 0: Trigger input lines in AUTO and FLASH mode: Start the flash, when all of the input lines are set high E.g.: respond to Trigger when line 0 and 3 are set: 1+8 = 9 If set, register 21 is set to 0	1: Trigger line 0 2: Trigger line 1 4: Trigger line 2 8: Trigger line 3	R/W/E	same as channel
23	1	Channel 0: Target current [mA] Min: 50 mA Max: see input register 10 If LED is connected: Auto or Steady mode: tune and set current immediately In Flash mode: tune after current and set for next flash	mA	R/W/E	150
24 - 25	2	Channel 0: Target flash length [μs], Min: see input register #11 Max: see input register #12	μs	R/W/E	20
26 - 29	3	Reserved	-	-	-
30 – 39	10	Channel 1 specific			
40 – 49	10	Channel 2 specific			
50 – 59	10	Channel 3 specific			

Input Register: Function code 04 (read)

Address (decimal)	Points	Description	Data format	R/W/E	Factory Default
00	1	Number of available light channels	Number	R	4
01	1	HW version (e.g. 3.012, 0x030C)	High byte: Major release (e.g. 3) Low byte: Minor release (e.g. 12)	R	-
02	1	Firmware version (e.g. 1.007)	High byte: Major release (e.g. 1) Low byte 1: Minor release (e.g. 7) 0x0107	R	-
03	1	IO and protocol version (e.g. 1.000)	High byte: Major release (e.g. 1) Low byte: Minor release (e.g. 0) 0x0100	R	1.000
04	1	Production week (batch number) (e.g. 2020 KW35: 20-35 -> 0x1423)	High byte: Year Low byte: Week	R	-
05	1	Error Code	Bit1: Ch1 error Bit2: Ch2 error Bit3: Ch3 error Bit4: Ch4 error Bit8: DAC error	R	0
06 – 07	2	Debug output register	internal	R	-
08	1	Hardware trigger line input status	Bit 0: Trigger 0 Bit 1: Trigger 1 ...	R	0
09	1	Min Current [mA]	mA	R	50
10	1	Max Current [mA] per channel in continuous or auto mode	mA	R	X
11	1	Max Current in flash mode at minimal flash time	mA	R	21000
12	1	Min Flash time [µs]	µs	R	3 µs
13 – 14	2	Max Flash time [µs]	µs	R	50.000µs
15	1	Max Capacity per channel [mAs] in flash mode (current x time)	mAs	R	
16	1	Number of boot sequences	Number	R	-
17 – 19	3	Reserved	-	R	-
Channel specific					
20	1	Channel 0: LED current [mA]. STEADY or AUTO: actual current FLASH MODE: last flash current	mA	R	-
21	1	Channel 0: Last flash time [µs]	µs	R	
22	1	Channel 0: Voltage [mV] STEADY or AUTO: actual voltage FLASH MODE: last flash voltage	mV	R	

23	1	Channel 0: Tuning status	0: tuning never started since boot 1: tuning in progress 2: last tuning successful 3: last tuning failed	R	0
24	1	Channel 0: Channel specific error code. If set, 05 is set, too.	0x80: current not reached 0xff: short circuit	R	0
25 – 29	5	Reserved	-	-	-
30 – 39	10	Channel 1 specific			
40 – 49	10	Channel 2 specific			
50 – 59	10	Channel 3 specific			

Error Codes

Code (hex)	Decimal	Description	Remediation
0x0000	0	No Error	No applicable
0x0080	128	Current not reached	
0x00ff	255	Short circuit on channel output	

Blink Codes

permanent off: no LED
 permanent on: steady or auto LED is ON
 short blink: ~200ms; current tuning failed
 medium blink: ~500ms; LED detected, but not tuned
 long blink: ~750ms; LED detected and current tuned